

Paul A. Farrar et al.  
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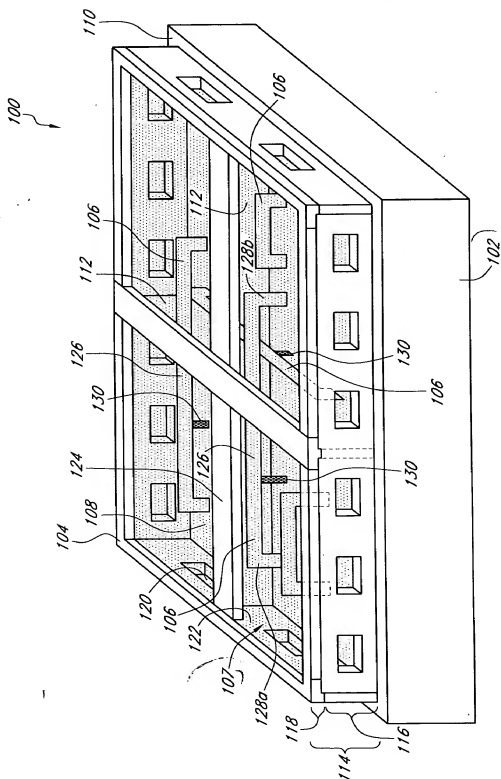


FIG. 1

MULTI-CHIP ELECTRONIC PACKAGE AND COOLING SYSTEM

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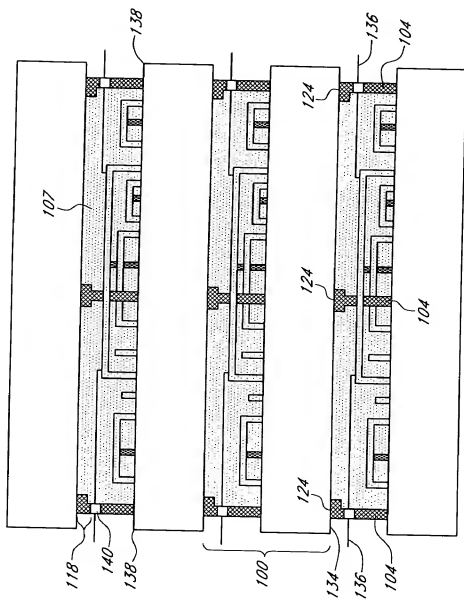


FIG. 2



# MULTI-CHIP ELECTRONIC PACKAGE AND COOLING SYSTEM

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FIG. 4

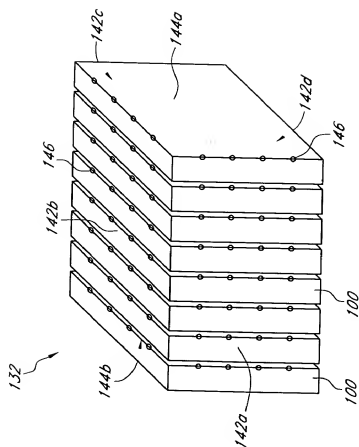
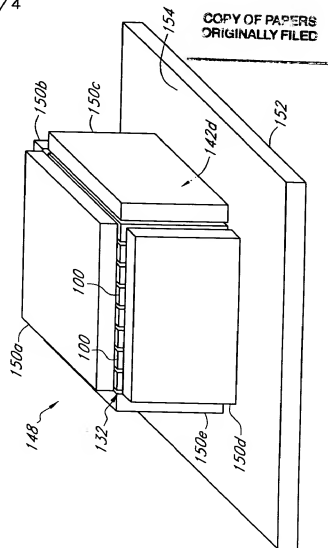


FIG. 3



MULTI-CHIP ELECTRONIC PACKAGE AND COOLING SYSTEM

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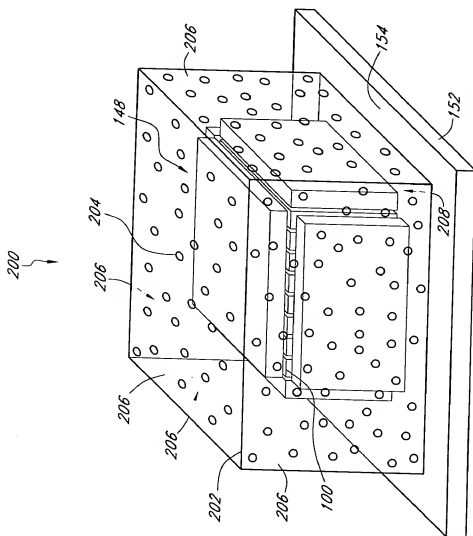


FIG. 5